

Digital Wireless Sensor Server Using an Adaptive Smart-Antenna/Retrodirective Array

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Abstract—A wireless sensor server is developed based on a reconfigurable active smart-antenna/retrodirective array. The system can serve as both a retrodirective array transponder and a smart-antenna receiver simply by changing the frequency of the local oscillator applied to the mixers, enabling it to best utilize its hardware to suit its communication environment. When operating as a direct-conversion receiver, the receiver array successfully demodulates a quaternary phase-shift keying (QPSK) modulated signal with circuit gain of 7 dB and E_b/N_0 for BER = 10^{-4} is approximately 12 dB without any error correction. In the retrodirective array mode, the system provides 20-dB circuit gain and 20-dB radio-frequency–intermediate-frequency isolation at the center frequency as well as phase conjugation, exhibiting excellent retrodirectivity. The mixers perform phase conjugation and modulation simultaneously, enabling the transmission of locally stored data. The local data is successfully extracted by an interrogator.

Index Terms—Digital communication, microwave receivers, phase conjugation, phased arrays, transponders.

I. INTRODUCTION

IN ORDER TO accommodate the need for high channel capacity in wireless communication systems, several techniques for efficient data multiplexing have been developed. Many of these schemes rely on multiplexing not only in time and frequency spectrum, but also in space. For this reason, multiband or wide-band multibeam array antennas are essential for optimal service area coverage. Smart-antenna technology was developed to satisfy these requirements. The use of a smart antenna increases the number of simultaneous users by using space-division multiple-access (SDMA) multiplexing, as well as providing a way to reduce the effect of multipath [1]–[3]. In a typical smart-antenna system, an antenna array is used as the radio-frequency (RF) frontend and signals from each individual element of the antenna array are combined after weighting functions are applied, such that a desired antenna beam pattern is formed. These weighting coefficients are determined by digital signal processing (DSP) and can be applied in either analog or digital domains [4].

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A retrodirective array is a purely analog self-phasing array that transmits a signal toward the source when it is interrogated. This is done using the microwave phase conjugation technique [5]–[11]. Using this technique, the main beam of the retrodirective array automatically tracks the position of the interrogator. There is no need to rely on complex DSP algorithms and circuitry to determine the direction-of-arrival (DOA) and signal-weighting functions. This is especially useful when the communication environment is simple, as in communications in line of sight. Therefore, the retrodirective array is a good choice for remote data retrieval-on-demand applications where multipath effects are not significant.

Reconfigurability brings about significant advantages in RF systems [12]. Because it allows front-end circuitry to share many components, whereby the circuit size and cost can be reduced significantly. The recent technology improvements in microwave devices and DSP chips has made it very easy to implement this kind of time-varying system. Integrating a direct-conversion receiver function followed by digital signal processing into the retrodirective array with reconfigurability, the array can be used as a semiduplex communication system. This function allows the received signals to be processed in the digital domain, where sophisticated algorithms can be used to better cope with multiple users or multipath environment issues, which is often seen in ground-to-ground communications. At the same time, the interrogator can extract the stored data, taking advantage of the retrodirective array feature.

In this paper, a multifunctional array system using both retrodirective array and smart-antenna technologies is introduced. While using the same circuit hardware, the system can be reconfigured just by switching the frequency of the local oscillator (LO). The retrodirective array mode is used for data extraction, which is a ground-to-air communication while the smart-antenna mode is used for ground-to-ground multiuser communications. The system can efficiently be used for mobile communication or high-end wireless sensor systems.

II. PROPOSED SYSTEM

Fig. 1 shows the proposed wireless sensor system using a reconfigurable active retrodirective/smart-antenna array. In this scenario, the proposed multifunctional array is used as a transponder between deployed sensors and an interrogator. In the data collection mode (a), the array system works as a smart antenna and is used to collect data from various sensors. The circuit on each element in the array serves as a direct-conversion receiver and the downconverted base-band signals are sent to analog-to-digital converters for digital beamforming

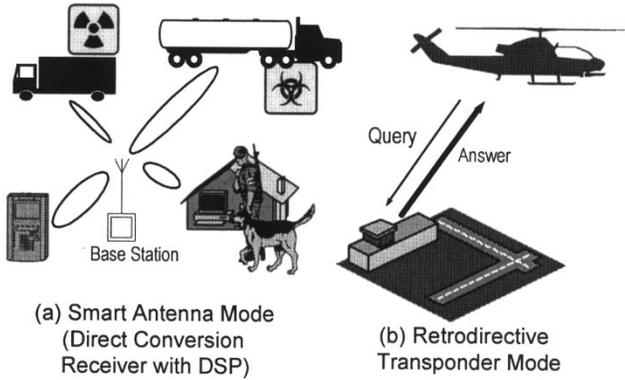


Fig. 1. Proposed sensor system.

processing and data recovery storage. The use of sophisticated algorithms allows the system to handle multiple sensor clients simultaneously, enabling SDMA.

Next, upon receiving an instruction signal from an interrogator, the system switches to retrodirective transponder mode (b) and sends the stored data to the interrogator. This switching is initiated by the header code contained in the signal from the interrogator. The retrodirective array always directs its peak beam toward the interrogator without having to identify its exact location. Contrary to a typical smart-antenna system, in this operating mode the system is able to track the target, relying solely on analog circuitry without the need for high-speed analog-to-digital converters, digital signal processing, and all the associated hardware circuitry. This greatly simplifies the overall system.

III. CIRCUIT OVERVIEW

The schematic of the multifunctional circuit is shown in Fig. 2. The received signal is first amplified by a low-noise amplifier (LNA) and applied to the resistive field-effect transistor (FET) mixers in phase through a ratrace coupler. The use of resistive mixers minimize the direct-current (dc) separation issue. It also reduces dc power consumption as almost no current flows when no signal is applied. The mixers are fed by an LO signal in a phase relationship set by a phase-delay line. The phase delay is 180° at 11.6 GHz and 45° at 2.9 GHz. When a 2.9 GHz LO is applied, the circuit works as a subharmonic direct-conversion receiver (smart-antenna mode). When a 11.6-GHz LO is applied, the same circuit serves as a phase conjugator (retrodirective transponder mode).

A prototype circuit was fabricated on RT/duroid 6010 (0.635-mm thickness, dielectric constant $\epsilon_r = 10.2$) substrate, as shown in Fig. 3. The heterodyne FET mixers employ NEC NE76038 Gallium Arsenide (GaAs) metal semiconductor field-effect transistors (MESFETs) while the LNAs are Agilent MGA-86576. GaAs MESFETs are economical as they provide high gain and low noise at higher frequencies. The approximate dc power consumption by the prototype circuit is 160 mW. The circuit size is approximately $9 \text{ cm} \times 2.5 \text{ cm}$. It is small enough to maintain half-wavelength array spacing, which is very important in order to avoid grating lobes.

A. Direct-Conversion Receiver Mode (Smart-Antenna Mode)

In the receiver mode, the received signal is first amplified by a LNA, then downconverted directly to baseband. A LO signal

at half the frequency of the received RF signal is applied to the mixers with a 45° phase difference, enabling quadrature direct-conversion mixing. I and Q channels are obtained through lumped element lowpass filters from the drain sides of the resistive FET mixers. The direct-conversion receiver can accommodate quadrature type of modulation schemes, enabling more efficient data transmission. In addition, the image problem is eliminated in the direct-conversion scheme, eliminating the need for image-rejection filters.

First, the direct-conversion receiver was evaluated by applying an unmodulated RF signal (-50 dBm) at 5.79 GHz and an LO (10 dBm) at 2.8975 GHz ($2.895 \text{ GHz} + 2.5 \text{ MHz}$) so that the residual carrier of 5 MHz can be observed at the I/Q ports. Figs. 4 and 5 show intermediate-frequency (IF) power versus RF input power and the circuit gain versus LO input power plots. Since LNAs are integrated into the mixer circuit, signal gain is available although the mixers themselves have conversion loss. For 0-dB loss, the minimum LO power required is 2 dBm. The maximum circuit gain is obtained with LO power of 13 dBm. Fig. 6 shows the quadrature phase difference between two channels at an IF frequency of 5 MHz. The phase imbalance is within approximately 10° .

Next, a demodulation performance test is carried out. A QPSK-modulated RF signal is applied into the circuit. In this measurement, a RF signal applied to the circuit is QPSK-modulated by 10 MHz. For simplicity, identical "1010" signals generated by a function generator are applied to the I and Q channels. The RF center frequency is 5.79 GHz and the LO frequency is 2.895 GHz. Since the LO frequency is exactly half the RF frequency, there is no IF signal in this case. Therefore, the modulated RF signal is downconverted directly to baseband without any IF. Fig. 7 shows the demodulated baseband signals from the I and Q ports. The digital base-band signal was successfully recovered.

B. Phase-Conjugating Mode (Retrodirective Transponder Mode)

In this operating mode, LO frequency is switched to twice that of the received interrogating signal. The RF and IF frequencies are close together while the LO frequency is far from both frequencies. Therefore, it is possible to reduce the circuit size by applying RF and LO from the opposite sides of a FET and extracting the IF product from the RF input port [11]. The matching networks for RF and IF signals can be shared. There is no need for any coupler to combine signals at different frequencies. However, this architecture makes it difficult to insert additional active components, such as amplifiers. This issue is overcome by using a balanced architecture fed by a ratrace coupler. The RF signal is applied into the mixers in phase; hence, the rejected RF signals are also in phase while the phase-conjugated signals are 180° out of phase. The fundamental mixing process can be given by (1) and (2) as

$$\begin{aligned} \text{Ch1: } V_{\text{IF1}}(t) &= V_{\text{RF}} \cos(\omega_{\text{RF}}t + \theta_n) \cdot V_{\text{LO}} \cos(\omega_{\text{LO}}t) \\ &= \frac{1}{2} V_{\text{RF}} V_{\text{LO}} [\cos((\omega_{\text{LO}} - \omega_{\text{RF}})t - \theta_n) \\ &\quad + \cos((\omega_{\text{LO}} + \omega_{\text{RF}})t + \theta_n)] \quad (1) \end{aligned}$$

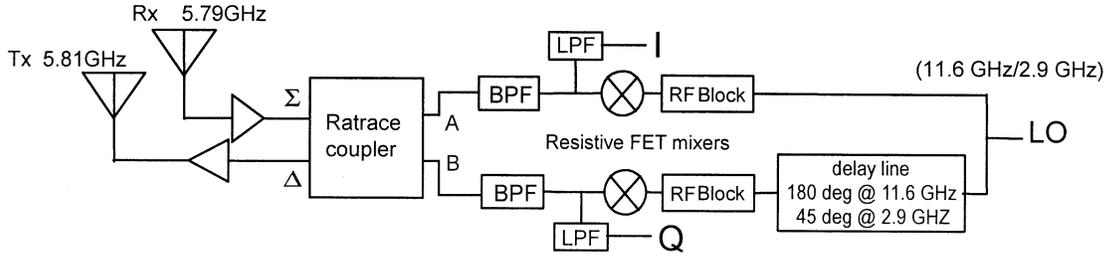


Fig. 2. Schematic of the multifunctional circuit.

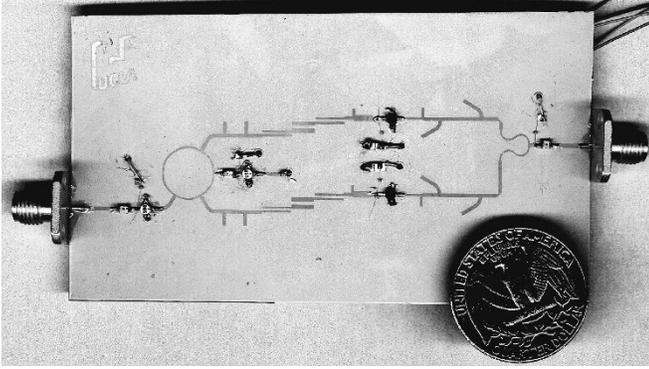


Fig. 3. Fabricated multifunctional circuit.

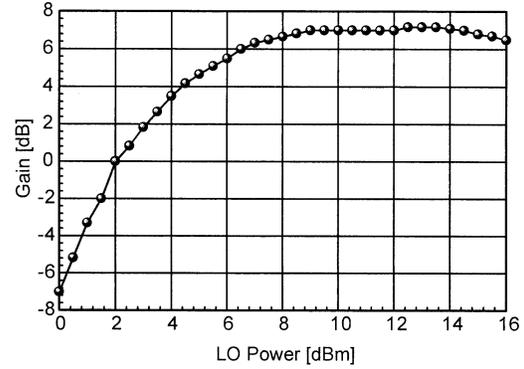


Fig. 5. Gain versus LO power.

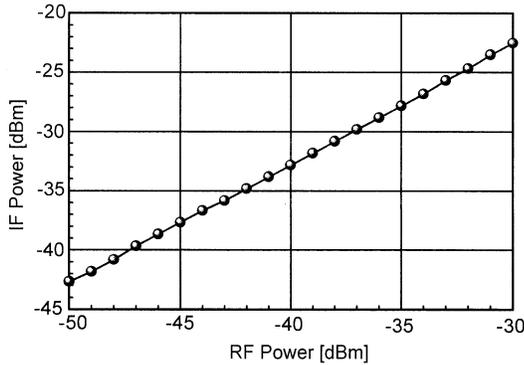


Fig. 4. IF power versus RF power.

$$\begin{aligned}
 \text{Ch2: } V_{IF2}(t) &= V_{RF} \cos(\omega_{RF}t + \theta_n) \cdot V_{LO} \cos(\omega_{LO}t - \pi) \\
 &= \frac{1}{2} V_{RF} V_{LO} [\cos((\omega_{LO} - \omega_{RF})t - \theta_n - \pi) \\
 &\quad + \cos((\omega_{LO} + \omega_{RF})t + \theta_n + \pi)].
 \end{aligned} \tag{2}$$

If the LO frequency is set such that $\omega_{LO} = 2\omega_{RF}$, the lower side band signal at $\omega_{LO} - \omega_{RF}$ is the phase conjugation of the RF signal. Since the LO signal is applied out of phase to the FETs, the phase-conjugated signals from the two channels are antiphase while the RF leakage is in phase. Thus, at the Δ port of the ratraace coupler, the rejected RF should cancel out and only the phase-conjugated signal can pass on to the next stage, providing decent isolation between the phase-conjugated signal and RF leakage. The isolation is decided at this point and the signal can be amplified depending on the communication distance by inserting amplifiers between the output port and the transmitting antenna. The LO leakage and upper side band can easily be filtered out, since the frequencies are far from that of the phase-conjugated signal.

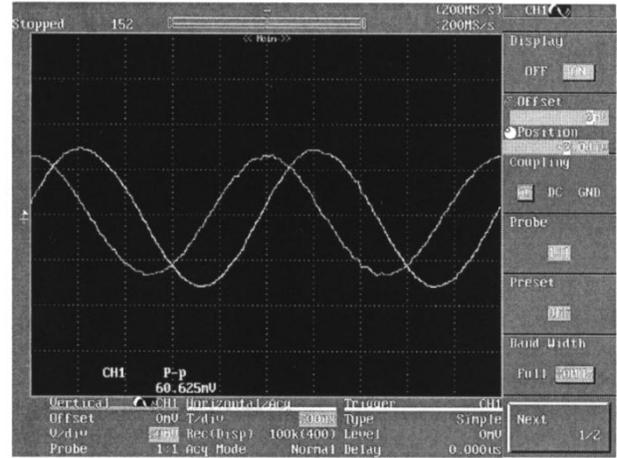


Fig. 6. Quadrature phase of the down-converted signals.

The circuit performance for the phase-conjugating mode is tested by using two synthesizers connected to the RF and LO input ports in order to provide the RF (-50 dBm, 5.79 GHz) and LO (10 dBm, 11.6 GHz) signals. A spectrum analyzer is connected to the output port to measure the signal spectrum. The phase-conjugated signal is at 5.81 GHz. Fig. 8 shows the circuit gain and the RF-IF isolation over the RF frequency range 5.75–5.81 GHz. The conversion gain is fairly flat and the RF-IF isolation stays below 15 dB over the range. The maximum RF-IF isolation of 24 dB is achieved at 5.77 GHz.

In order to simulate the scenario in which the array is interrogated by multiple users simultaneously, the circuit is tested using two RF tones. First, a RF signal is applied into the input port of the circuit. Fig. 9(a) is the power spectrum at the output port. Then, the second RF signal at a different frequency is ap-

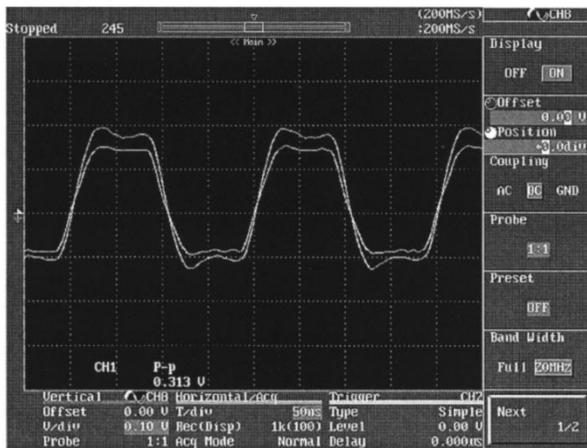
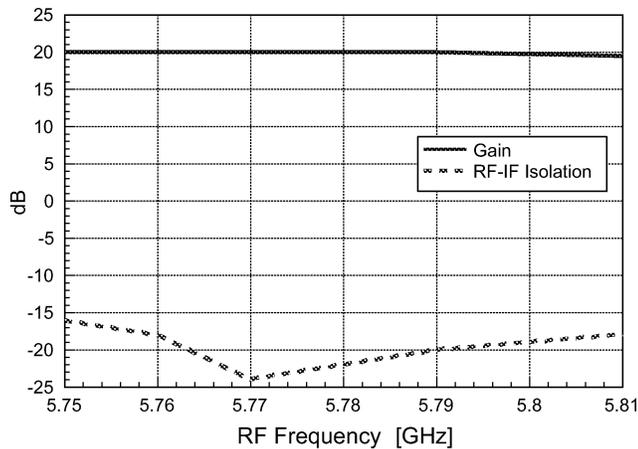
Fig. 7. Demodulated I and Q waveforms (10 Mb/s).

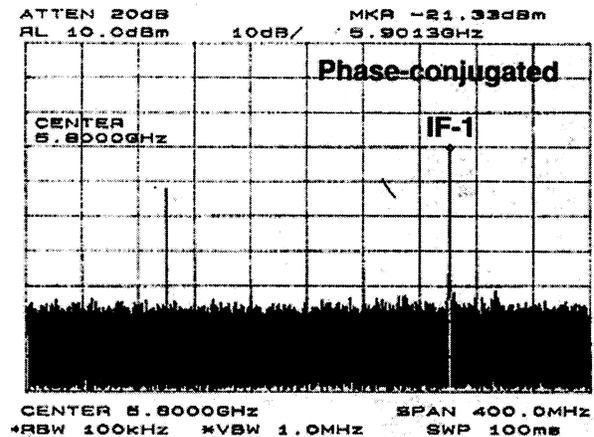
Fig. 8. Bandwidth of the circuit.

plied to the circuit. Fig. 9(b) shows output power spectrum of the two-tone interrogation case. The power of IF-1 stays at the same power level in both cases. These results show that two simultaneous interrogations do not make any significant performance change in this circuit.

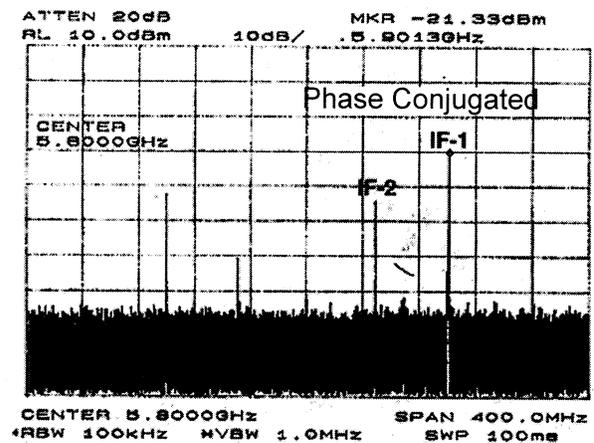
The phase-conjugated signal can be modulated by applying bipolar non-return to zero (NRZ) binary signals into the base-band ports. This feature allows the retrodirective array to transmit local information on demand. Modulation of the return signal sent by the retrodirective array is done by applying a bipolar NRZ signal at the FET mixer base-band ports. 1-Mb/s “1010” square waves generated by a function generator are applied into the base-band ports. The output spectrum shows a successful binary phase-shift keying (BPSK) modulation of the phase-conjugated signal. Also, the base-band signals are successfully recovered by a demodulator connected at the output.

IV. SYSTEM MEASUREMENTS

A prototype reconfigurable array based on the proposed multifunctional circuit is fabricated and shown in Fig. 10. It consists of two sets of four-element patch-antenna arrays. One array is used for receiving and the other for transmitting. Antennas with



(a)



(b)

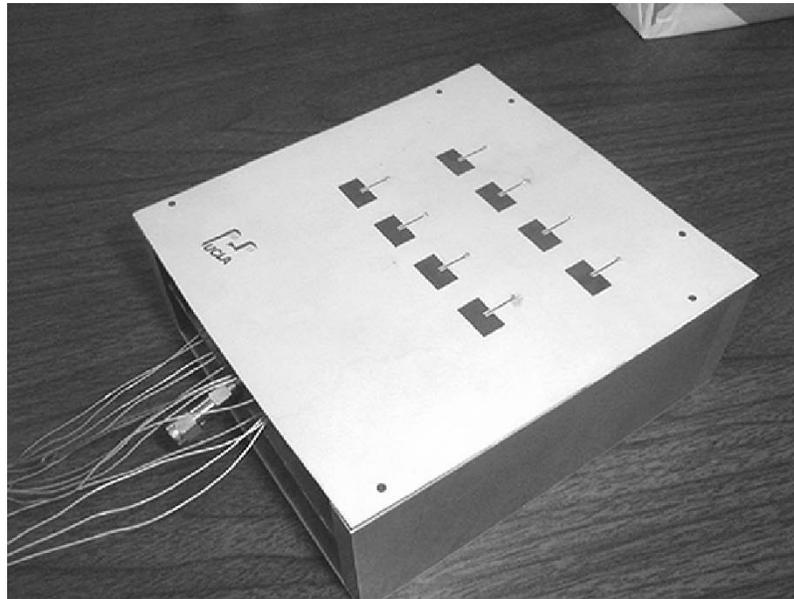
Fig. 9. Phase conjugation with multiple interrogations.

broad radiation patterns are more suited for phased arrays, since received power is fairly constant within the scanning range. The antennas are attached to the back side of the circuitry, reducing the physical size of the sensor server system. The array spacing is set to approximately 0.45 freespace wavelength, enabling an 180° scan without having grating lobes, which becomes visible in arrays with array spacing $d > \lambda_0 / (1 + |\sin \theta_{in}|)$ where θ_{in} is the incident angle of the interrogating signal. The circuitry size is approximately $10 \text{ cm} \times 10 \text{ cm}$.

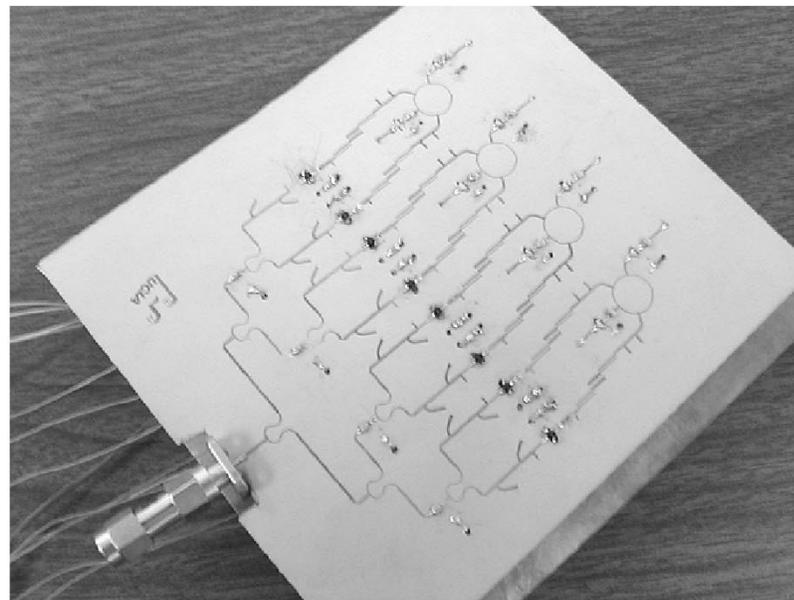
The system is evaluated in the two modes of operation: smart-antenna and retrodirective transponder. In the smart-antenna mode, bit-error rate (BER) measurements are carried out. The digital beam forming feature is tested using offline digital signal processing. As for the retrodirective transponder mode, the radar cross section (RCS) pattern is measured.

A. Smart-Antenna Mode

Fig. 11 shows the BER measurement setup. Both the receiver array and the transmitter are placed in an anechoic chamber. The receiver is located in the far field of the transmitting antenna. The receiver array is illuminated by a 5.79-GHz signal with a 2-Mb/s QPSK modulation from broadside. The level of noise generated by a noise source is controlled by an attenuator and



(a)



(b)

Fig. 10. Photos of the prototype four-element array system. (a) Antenna and (b) circuit sides.

combined with the RF signal. An NRZ base-band signal is generated by a BER tester. The data rate, 2 Mb/s, is selected due to the limit of the bit-error tester. The received signal is downconverted to baseband and the recovered base-band signal is amplified before being sent back to the BER tester along a coaxial cable. The theoretical BER of QPSK modulation is given by [13]

$$P_e = \frac{1}{2} \operatorname{erfc} \left(\sqrt{\frac{E_b}{N_0}} \right)$$

where $\operatorname{erfc}(x) = \frac{2}{\sqrt{\pi}} \int_x^\infty \exp(-t^2) dt$. (3)

Fig. 12 shows the BER measurement results and theoretical estimations under different SNR conditions. E_b/N_0 is measured at the transmitting antenna. The noise figure of the low-noise amplifiers is approximately 2.3 dB at this frequency. Therefore, the measurement results reasonably match the theory. E_b/N_0 for $\operatorname{BER} = 10^{-4}$ is approximately 12 dB without any error correction.

Next, the array is illuminated by a 5.79-GHz wave coming from an angle and the LO frequency is $2.895 + \delta$ (offset) GHz, allowing the baseband to have a residual frequency. The signal is downconverted at each element and the downconverted signal

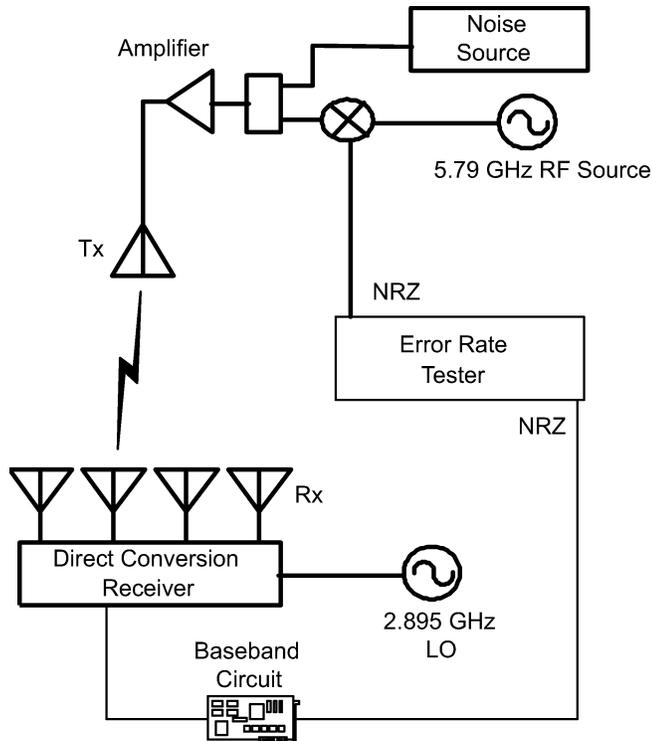


Fig. 11. BER measurement setup.

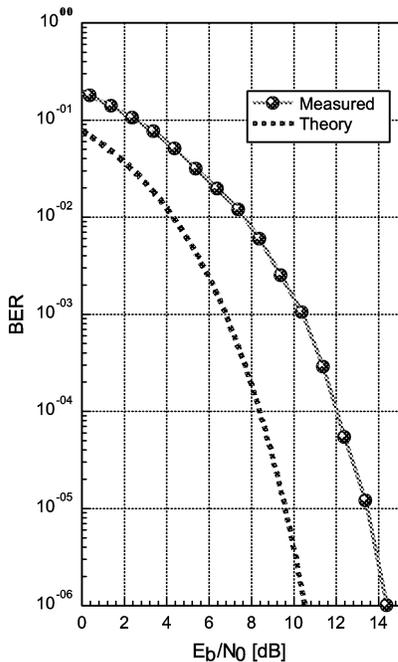


Fig. 12. BER versus SNR.

is sampled by using a digital oscilloscope. The sampled data is processed by a MATLAB program on a PC for digital beamforming using the MUSIC algorithm [2], [14]. Fig. 13(a) shows the normalized sampled data. DOA estimation is done using the MUSIC algorithm as shown in Fig. 13(b). Weighting functions are created based on the DOA estimation and applied to the data such that the main peak is at the incoming angle. Fig. 13(c) shows the comparison between the digital beam formed and raw

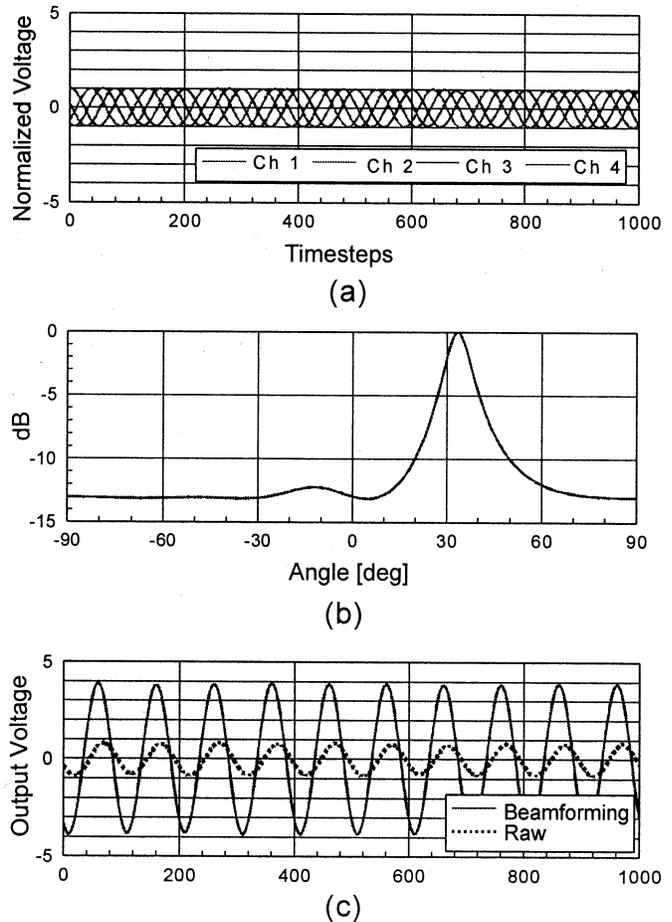


Fig. 13. Digital beamforming process for (a) sampled data, (b) DOA estimation, and (c) digital beamforming results.

data (combined without beamforming). Since the signal from each channel is coherent after the weighting function is applied, the signals are combined coherently to obtain higher amplitude. MUSIC DOA estimation can also be used to suppress undesired signals, reducing cochannel interference and intersymbol interference for multiuser systems.

B. Retrodirective Transponder Mode

To confirm the retrodirectivity, the monostatic radar cross section (RCS) pattern measurement is done. In this measurement, the array is illuminated with a 5.79-GHz wave generated by a synthesizer and was driven by an 11.6-GHz LO signal. The array is placed in the far zone of the transmitting horn antenna. The IF (phase-conjugated signal) power is measured by another horn antenna collocated with the transmitting antenna. Because the receiver antenna is collocated with the interrogator, it always "sees" the peak of the array radiation. The monostatic RCS pattern should not exhibit any nulls. Fig. 14 shows the monostatic RCS pattern. The monostatic RCS pattern is given by

$$\sigma_{\text{monostatic}} = \frac{\lambda^2}{4\pi} D_0(\theta_{\text{in}}) G_c D_e^2(\theta_{\text{in}}) \quad (4)$$

where D_0 is the directivity of the array, G_c is the circuit gain, D_e is the directivity of the patch-antenna element. Note that

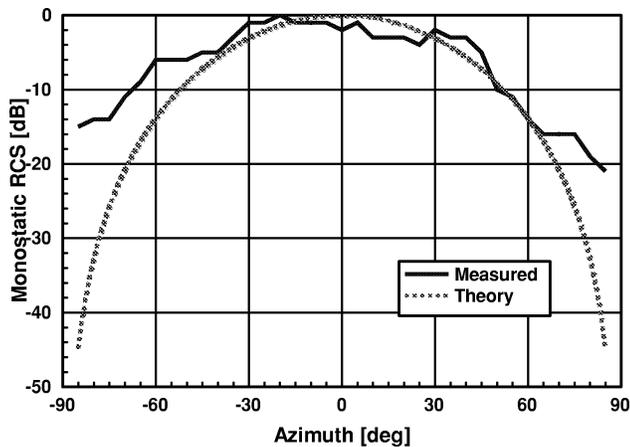


Fig. 14. Monostatic RCS pattern.

the array factor is not squared. This is because, in the receiving process, each element is separately processed without being combined. The antennas are not an array, but merely four separate antennas. The normalized monostatic RCS pattern is given by the multiplication of the array directivity and the square of the antenna directivity.

Normally, the gain of an array is constant around the broadside and higher at sharp angles, i.e., far from the broadside when it is scanned. Therefore, the monostatic RCS of the retrodirective array is flatter than the radiation pattern of the antenna element. The results agree reasonably well with the theoretical prediction. As shown in the measured results, the monostatic RCS of the retrodirective array has no nulls.

V. CONCLUSION

A reconfigurable smart-antenna/retrodirective array for wireless sensor server applications has been developed. The system operates in the 5.8-GHz industrial, scientific, and medical (ISM) band. The compact circuitry serves as both a phase-conjugator and a direct-conversion receiver. It can be reconfigured simply by switching the LO frequency. In the smart-antenna mode, 10-Mb/s QPSK signals are successfully recovered by the direct-conversion circuitry. E_b/N_0 for BER = 10^{-4} is approximately 12 dB without error correction. The digital beamforming feature takes advantage of SDMA; therefore, it increases the number of server clients handled by the server. In the retrodirective transponder mode, Circuit gain of 20 dB is obtained while good RF-IF isolation is maintained. The phase conjugated signal is successfully modulated by a 10-Mb/s data signal. The monostatic RCS shows a flat characteristic due to the excellent retrodirectivity.

The proposed system should also find a wide variety of applications as well as for wireless sensor servers. By applying time-division duplexing (TDD) and improving the base-band circuitry, it can be used for other digital mobile communications such as wireless local networks (LAN).

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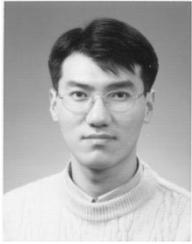
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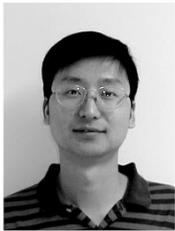


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Dr. Itoh is a Member of the Institute of Electronics and Communication Engineers of Japan, and Commissions B and D of USNC/URSI. He served as the Editor of IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES for 1983-1985. He serves on the Administrative Committee of IEEE Microwave Theory and Techniques Society. He was Vice President of the Microwave Theory and Techniques Society in 1989 and President in 1990. He was the Editor-in-Chief of IEEE MICROWAVE AND GUIDED WAVE LETTERS from 1991 through 1994. He was elected as an Honorary Life Member of MTT Society in 1994. He was the Chairman of USNC/URSI Commission D from 1988 to 1990, and Chairman of Commission D of the International URSI for 1993-1996. He is Chair of Long Range Planning Committee of URSI. He serves on advisory boards and committees of a number of organizations. He received a number of awards, including Shida Award from Japanese Ministry of Post and Telecommunications in 1998, Japan Microwave Prize in 1998, IEEE Third Millennium Medal in 2000, and IEEE MTT Distinguished Educator Award in 2000. He was elected to a member of National Academy of Engineering in 2003.